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| | | Application Number | 09/844,672 |
| | | Filing Date | April 30, 2001 |
| | | First Named Inventor | THEKKATH |
| | | Group Art Unit | 2193-2191 |
| | | Examiner Name | Khatri, Anil |
| Sheet | 1 of 1 | Attorney Docket Number | MTEC-005/00US |

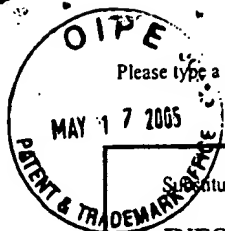
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| <i>n</i> | D1. | F. Chow et al., "Engineering a RISC Compiler System," <i>IEEE COMP-CON</i> , March 1986, pp. 132-137. | |
| <i>n</i> | D2. | M.D. Smith, "Tracing with Pixie," <i>Technical Report CSL-TR-91-497</i> , Stanford University, Computer Systems Laboratory, November 1991, pp. 1-29. | |
| <i>n</i> | D3. | ATOM Reference Manual, <i>Digital Equipment Corporation</i> , Massachusetts, December 1993, pp. 1-32. | |
| <i>n</i> | D4. | A. Srivastava et al., "ATOM: A System for Building Customized Program Analysis Tools," <i>WRL, Research Report 94/2</i> , Digital Equipment Corporation, Massachusetts, March 1994, pp. 1-23. | |
| <i>n</i> | D5. | ATOM User Manual, <i>Digital Equipment Corporation</i> , March 1994, pp. 1-28. | |
| <i>n</i> | D6. | B. Cmelik et al., "Shade: A Fast Instruction-Set Simulator for Execution Profiling," <i>Proceedings of the 1994 ACM SIGMETRICS Conference</i> , SIGMETRICS, California, May 1994, pp. 128-137. | |
| <i>n</i> | D7. | MIPS Technologies; "MIPS64 5K™ Processor Core Integrator's Guide;" [Document No. MD00056]; <i>MIPS Technologies, Inc.</i> ; Rev. 1.2; August 11, 2000; pp. I-II, 1-82. | |
| <i>n</i> | D8. | MIPS Technologies; "MIPS64™ 5K™ Processor Core Family Integrator's Guide;" [Document No. MD00106]; ©1999-2001 <i>MIPS Technologies, Inc.</i> ; Rev. 02.00; January 15, 2001; pp. I-VIII, 1-75. | |
| <i>n</i> | D9. | MIPS Technologies; "Core Processor Interface Specification;" [Document No. MD00068]; <i>MIPS Technologies, Inc.</i> ; Rev. 1.11; March 30, 2001; pp. 1-26. | |
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| | | Group Art Unit | 2124 2191 |
| Examiner Name | Khatri, Anil | | |
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| FOREIGN PATENT DOCUMENTS | | | | | | | |
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| Examiner Initials* | Cite No. ¹ | Foreign Patent Document | | | Name of Patentee or Applicant of Cited Document | Date of Publication of Cited Document MM-DD-YYYY | T ⁴ |
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|  | D1 | Susan J. EGGERS, et al., "Techniques for Efficient Inline Tracing on a Shared-Memory Multiprocessor," University of Washington, 1990 ACM, pp. 37-47. | | |
|  | D2 | Richard A. UHLIG, et al., "Trace-Driven Memory Simulation: A Survey," Intel Microcomputer Research Lab; University of Michigan, <i>ACM Computing Surveys</i> , Vol. 29, No. 2, June 1997, pp. 128-170. | | |
|  | D3 | Eric ROTENBERG, et al., "Trace Processors," University of Wisconsin, 1997 <i>IEEE Service Center</i> , 12 pp. | | |
|  | D4 | E.N. ELNOZAHY, "Address Trace Compression Through Loop Detection and Reduction," ©1999 ACM 1-58113-083, pp. 214, 215. | | |
|  | D5 | Darren JONES, "Opal Coprocessor Interface," <i>MIP Proprietary/Confidential</i> , June 4, 1999, pp. 1-18. | | |
|  | D6 | Darren JONES, <i>MIPSS64™ SKC™ Processor Cores User's Manual</i> , Rev. 1.0, July 4, 1999, pp. 6-1 to 6-26. | | |
|  | D7 | <i>MIPSS64™ SKC™ Processor Cores User's Manual</i> , Rev. 1.0.1., 1999, pp. 247-276. | | |
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